Q.P. Code: 20CS0512		R	20
F	Reg. No:		
SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) M.Tech I Year I Semester Regular & Supplementary Examinations May/June-2022			
SOFTWARE ENGINEERING AND TESTING (Computer Science and Engineering)			
	Time: 3 hours	Max. M	Marks: 60
(Answer all Five Units $5 \times 12 = 60$ Marks) UNIT-I			
1	a Define Software. How Software evolves for a period of cycle. Explain?	L2	6M
	<b>b</b> Explain the changing Nature of Software in brief.	L2	6M
	OR		
2	What is a layered Technology in Software Engineering? Explain.	L2	12M
	UNIT-II		
3	Define Requirement Engineering and explain about Requirements Engineering Tasks.	L2	12M
4	<b>OR</b> <b>a</b> Explain in detail about Communication Principles/Practices.	L2	
7	<ul><li>b Explain in detail about Planning Principles/Practices.</li></ul>	L2 L2	6M 6M
	UNIT-III		UTVE
5	<b>a</b> Brief out the analysis in Modeling and List out the elements of analysis model.	L2	<b>6M</b>
	<b>b</b> What is the importance of Scenario Based Diagram? Explain.	L1	<b>6M</b>
(	OR		
6	How Scenario based Modeling is used in Analysis and Design while modeling.	L2	12M
7	a Distinguish Testing and Debugging in detail.	L2	<b>6M</b>
	<b>b</b> How Bugs affect us from mild to catastrophic in our day-to-day life.	L3	6 <b>M</b>
	OR		
8	What is the use of CFG? Explain how the elements of CFG is used in Path Testing.	L1	12M
	UNIT-V		
9	How DFG is used in DFT. Explain the 2 types of DFG.	L2	12M
10	OR Explain in detail about Transaction Flow Testing Techniques	1.2	133.4
10	Explain in detail about Transaction Flow Testing Techniques. *** END ***	L2	12M
	Page 1 of 1		